Robert C. Colwell, Reg. No. 27,431   (650) 326-2400

Applicant: Yuichiro NAGASHIMA, *et al.*

Title: Storage System Control Method, Storage System, ...

Attorney Docket N . 16869S-093400

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W 1175

FIG. 1

Robert C. Colwell, Reg. No. 27,431   (650) 326-2400
Applicant: Yuichiro NAGASHIMA, et al.
Title: Storage System Control Method, Storage System, ...
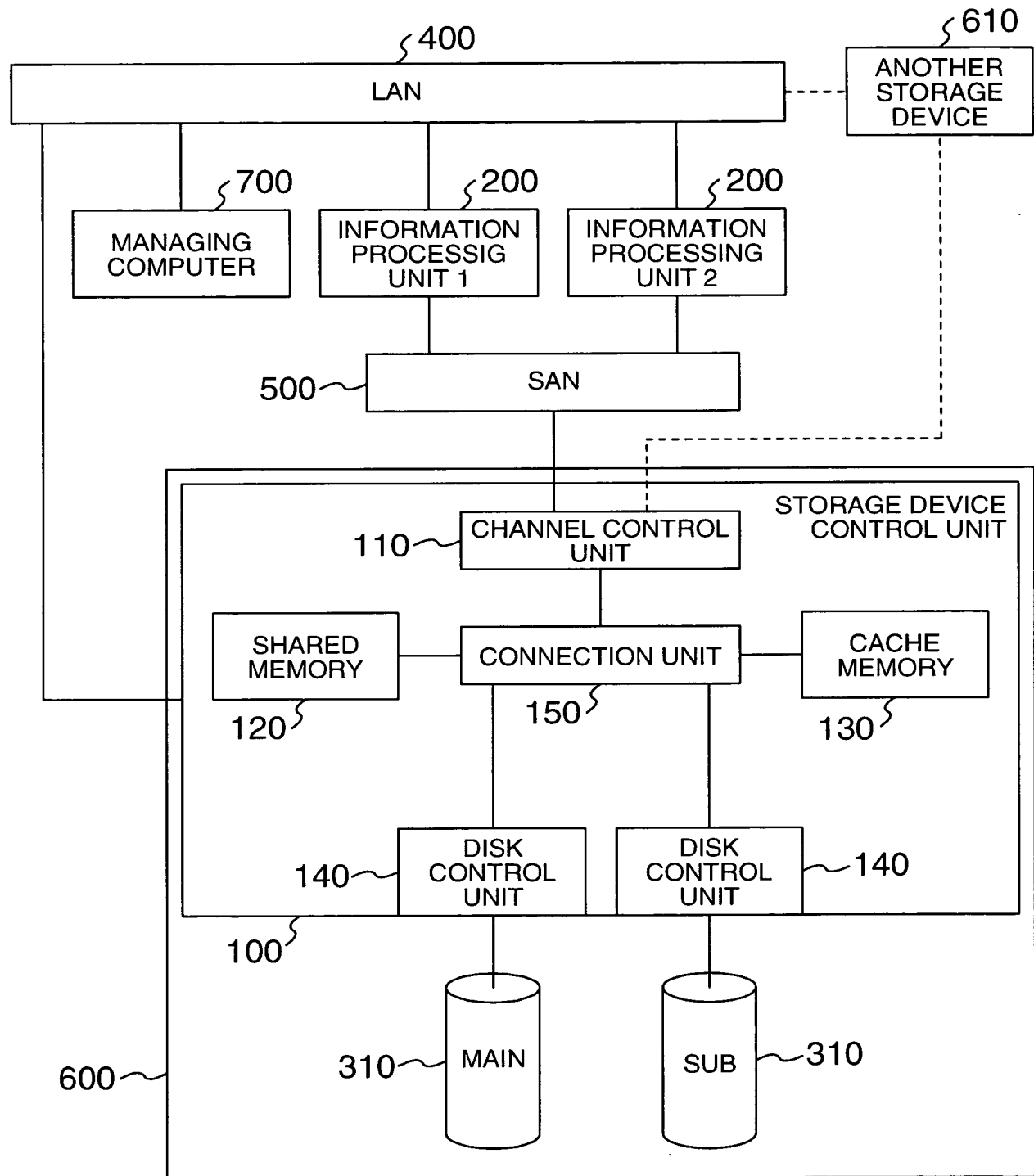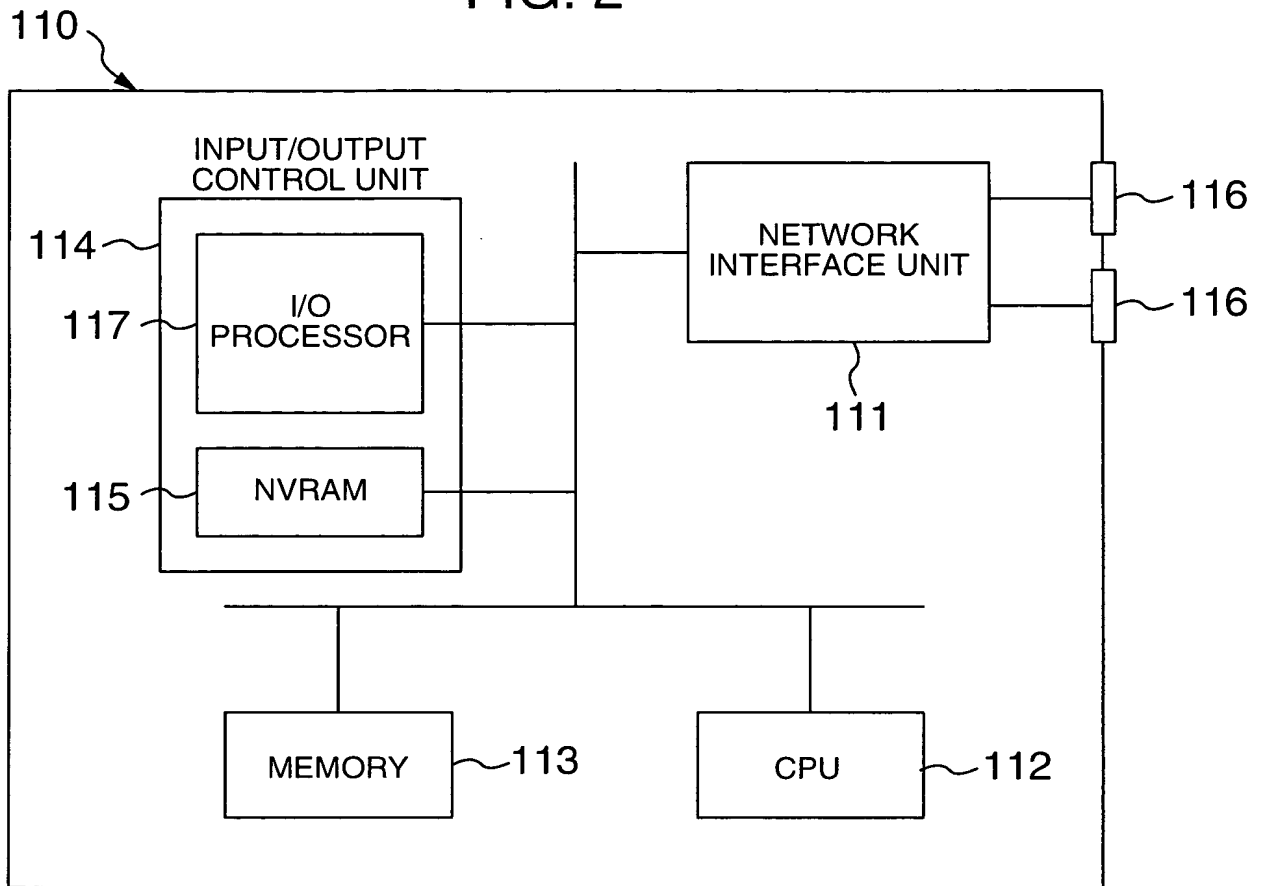Att rn y Docket No. 16869S-093400
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FIG. 2

110

INPUT/OUTPUT CONTROL UNIT

114

117 — I/O PROCESSOR

115 — NVRAM

NETWORK INTERFACE UNIT

116

116

111

MEMORY — 113

CPU — 112

FIG. 3

140

142 — CPU

144 — NVRAM

INTERFACE UNIT

141

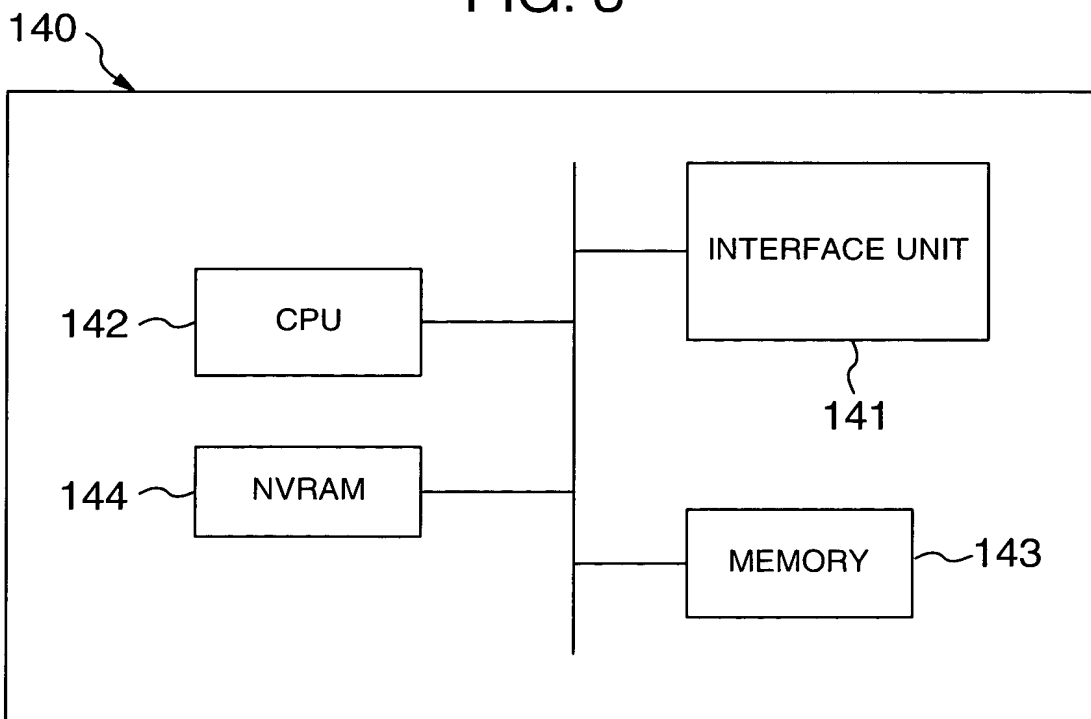MEMORY — 143

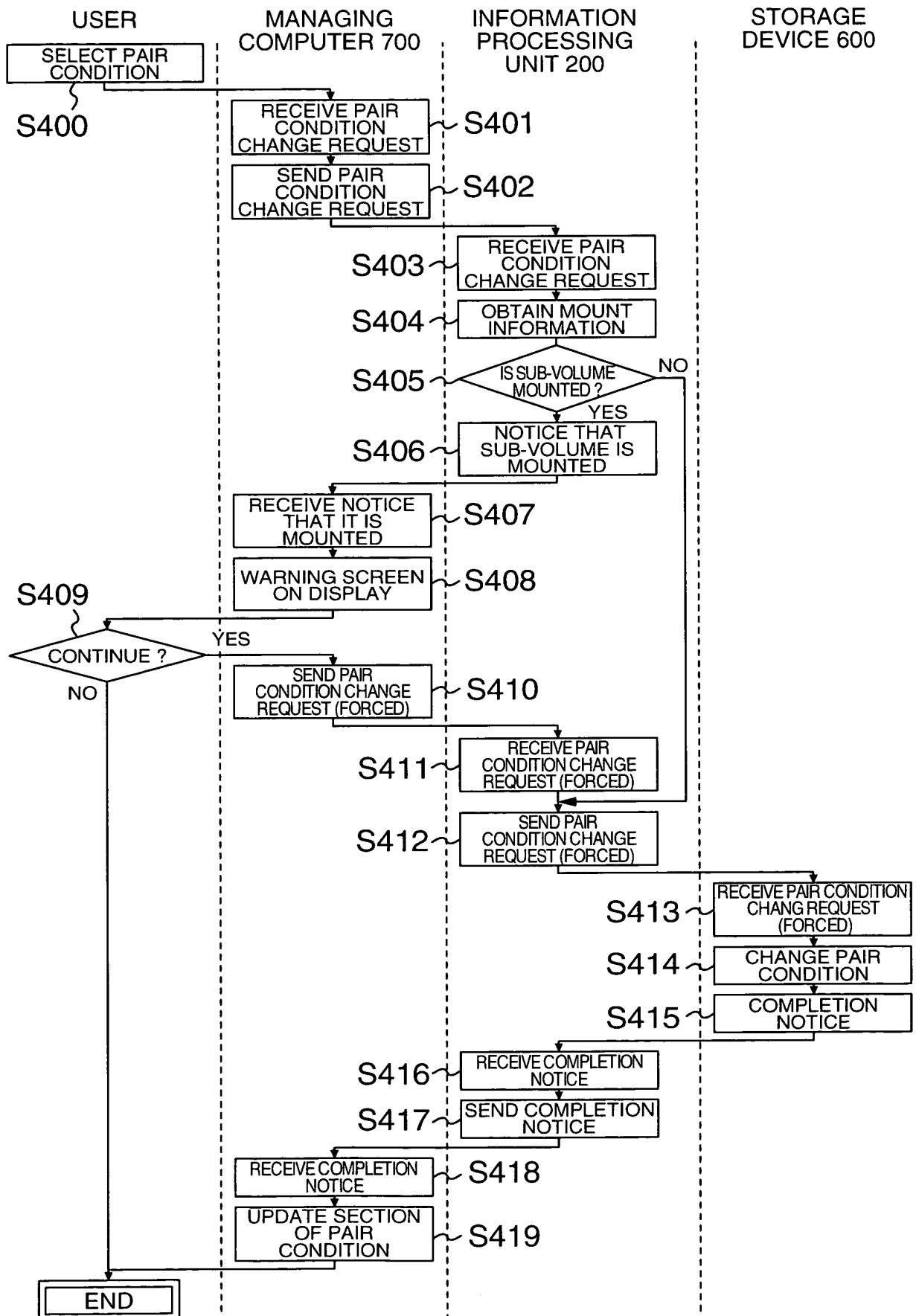Robert C. Colwell, Reg. No. 27,431   (650) 326-2400

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Titl : Storage System Control Method, Storage System, ...
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FIG. 4

FIG. 5

PAIR CONDITION CHANGE

STORAGE DEVICE	LOGICAL VOLUME	ROLE	CONDITION OF PAIR	CHANGE OF CONDITION	PAIRED LOGICAL VOLUME	
					STORAGE DEVICE	LOGICAL VOLUME
#A	#1	MAIN VOLUME	SPLIT CONDITION	SPLIT CONDITION ▶	#A	#2
#A	#2	SUB- VOLUME	SPLIT CONDITION	SPLIT CONDITION ▶	#A	#1
#A	#3	SUB- VOLUME	PAIR CONDITION	PAIR CONDITION / SPLIT CONDITION	#B	#1
#A	#4	—	—	—	—	—
#B	#1	MAIN VOLUME	PAIR CONDITION	PAIR CONDITION ▶	#A	#3

FIG. 6

WARNING

THIS SUB-VOLUME IS MOUNTED AT INFORMATION PROCESSING UNIT XX.

IF YOU PERFORM THIS OPERATION THERE IS A POSSIBILITY THAT
THE CONTENTS OF THE SUB-VOLUME ARE ERASED.

DO YOU WANT TO CONTINUE THE PROCESS? (y/n)

| Yes | No |

FIG. 7

CHANGE OF THE PAIR CONDITION IS COMPLETED.

| OK |